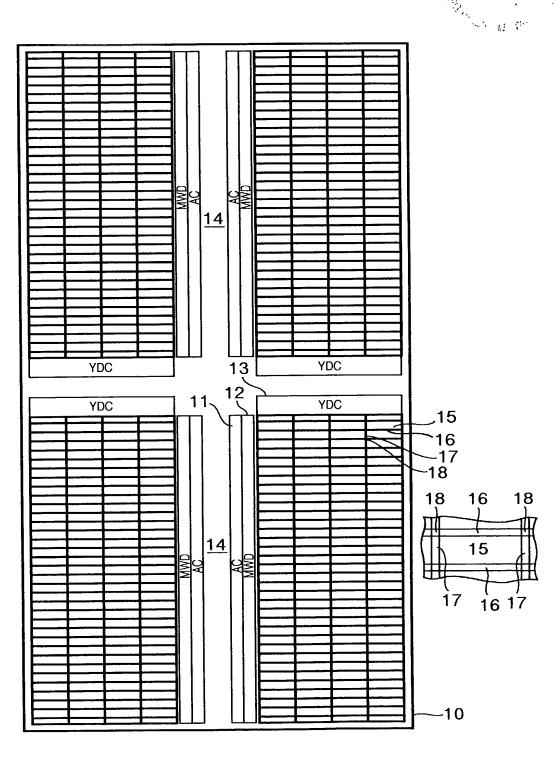
FIG.1





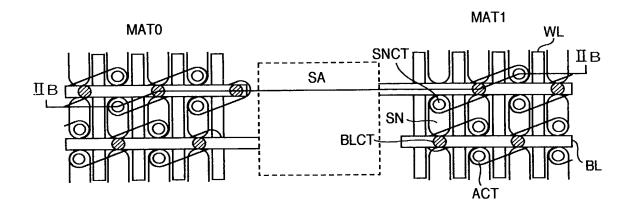
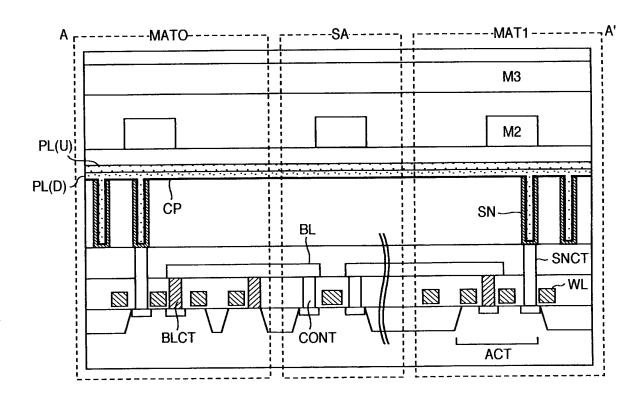
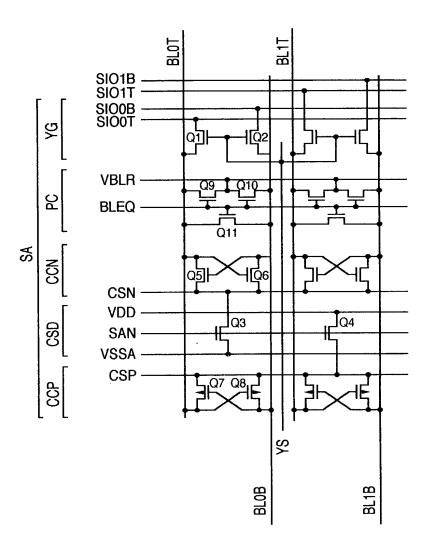


FIG.2B







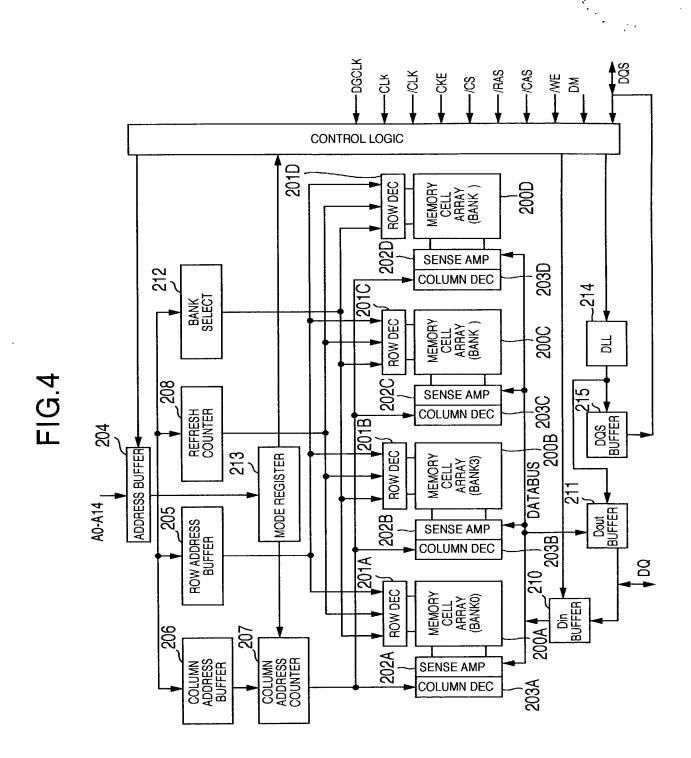
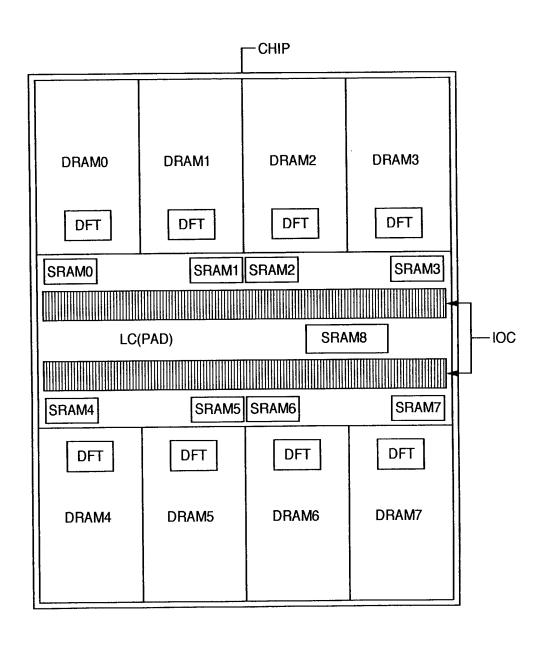


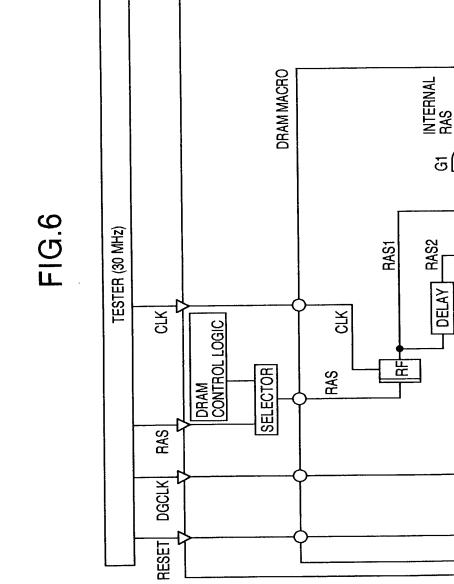
FIG.5



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INITIALIZE SIGNAL

DFT CIRCUIT TEST MODE FLAG



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FIG.7

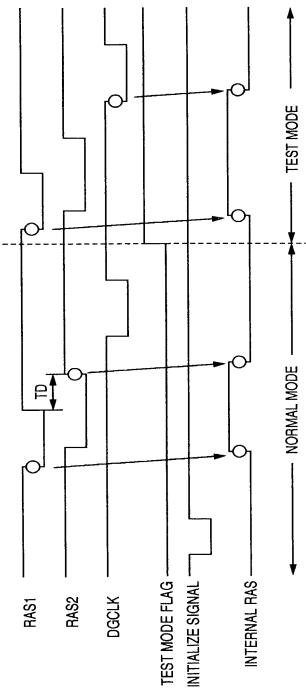
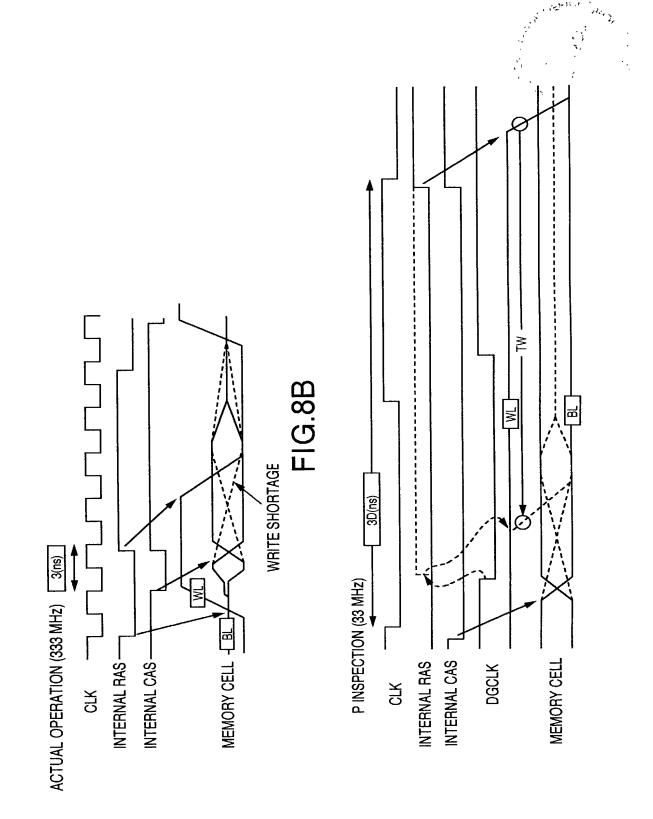
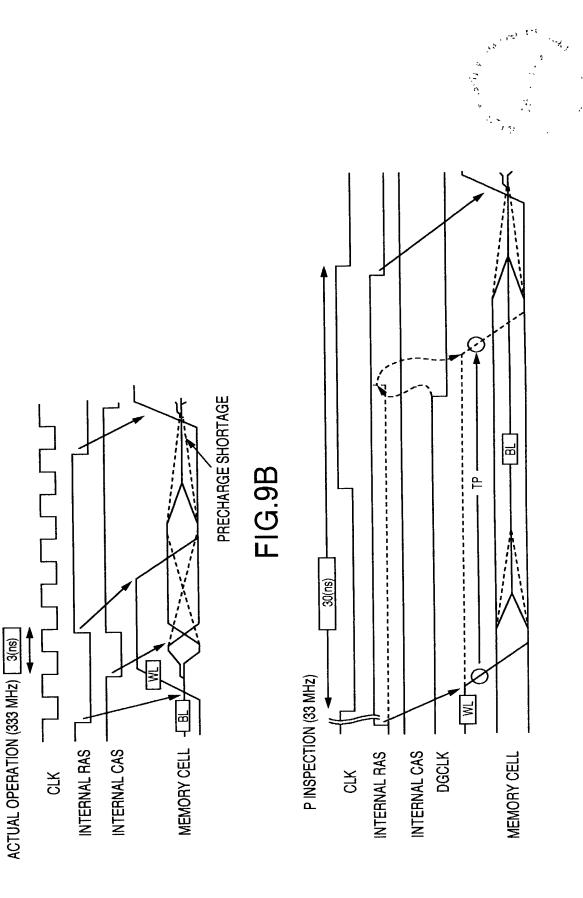
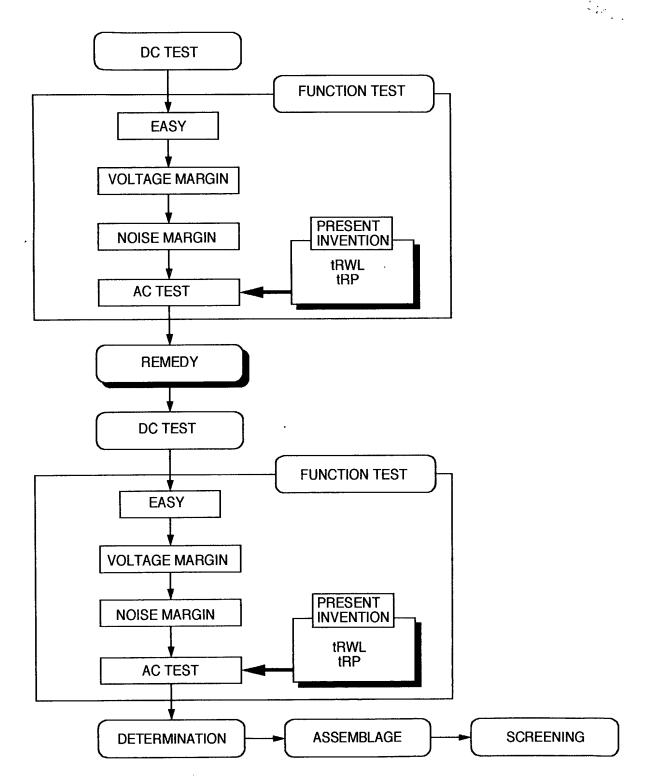


FIG.8A

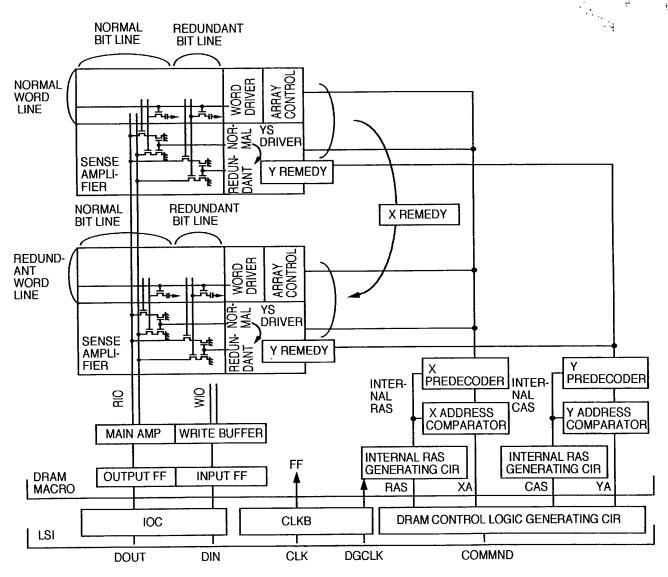




**FIG.10** 



## **FIG.11**



**FIG.12** 

